

**Notice of Allowability**

Application No.

10/642,261

Examiner

Dana Ross

Applicant(s)

ISHII, MITOSHI

Art Unit

3722

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Application filed August 18, 2003.
2. ☒ The allowed claim(s) is/are 1-7.
3. ☒ The drawings filed on 18 August 2003 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☐ Certified copies of the priority documents have been received.
  2. ☒ Certified copies of the priority documents have been received in Application No. 09/956,147.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 8/18/03
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

***ALLOWANCE***

**EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

The application has been amended as follows:

Disclosure:

First sentence of the specification after the title has been amended as follows: This application is a divisional application of prior application Serial No. 09/956,147 filed September 20, 2001 U.S. Pat. No. 6,671,940.

Abstract:

The object of the present invention is to form individual packaged ICs by using a single apparatus to cut wiring boards on which a plurality of IC chips are mounted. [

] The first cutting device 23 cuts the wiring boards 3 into separate parts along one direction, so that the cut wiring boards 3 constitute strip-form cut wiring boards 3a. The cut wiring boards 3a are conveyed to the second cutting device 33, and are fed [ into the second cutting device 33 ] by the second conveying device 43 along the direction of length[. [The cut wiring boards 3a] ] and are then cut by the second cutting device 33 with the orientation altered by 90 degrees relative to the first cutting direction. The wiring boards 3b resulting from the cutting performed by the second cutting device 33 constitute the original forms of the IC

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packages. The wiring boards 3b that have been cut and conveyed from the second cutting device 33 enter a cleaning step.

*Allowable Subject Matter*

2. Claim 1-7 are allowed.

3. The following is an examiner's statement of reasons for allowance: The prior art of record neither anticipates nor renders obvious a method for manufacturing electronic parts as claimed in claim 8. The closest prior art of record found is U.S. Pat. No. 6,205,656 (Adams et al.) which teaches an apparatus for manufacture of electronic parts which includes a cutter 32 which cuts electronic parts in the x and y directions through the use of a vacuum chuck 34 and a product accommodating device 41 (col. 3, lines 7-38, col. 4, lines 22-57).

Adams et al. does not teach the first and second cutting devices as claimed with the first and second fastening devices as claimed.

Therefore Adams et al. does anticipate the claimed invention of claim 1.

Furthermore, there is no prior art of record that renders obvious the claimed invention of claim 1 and no motivation found to modify Adams et al. to obtain the invention of claim 1.

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."


*Conclusion*

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dana Ross whose telephone number is (703) 305-7764. The examiner can normally be reached on Mon-Fri 7:00am - 3:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Andrea Wellington can be reached on (703) 308-2159. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

dmr

  
A. L. WELLINGTON  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 3700